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8021-205 (SS-17942-US)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Jeoung-Mo KOO et al. EXAMINER: Jose R. Diaz
SERIAL NO.: 10/799,201 GROUP ART UNIT: 2815
FILED: March 12, 2004
FOR: PROGRAMMABLE MEMORY DEVICE, INTEGRATED
CIRCUIT INCLUDING THE PROGRAMMABLE MEMORY
DEVICE, AND METHOD OF FABRICATING SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

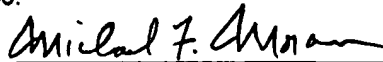
Sir:

This paper is submitted in response to the April 19, 2006 Office Action issued by the United States Patent and Trademark Office in connection with the above-identified application. A response to the April 19, 2006 Office Action is due on July 19, 2006. Therefore, this paper is being timely filed.

CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 19, 2006.

Dated: June 19, 2006


Michael F. Morano